

Docket No.: 43876-162

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Customer Number: 20277  
Craig HANSEN, et al. : Confirmation Number: To be assigned  
Serial No.: Unknown : Group Art Unit: To be assigned  
  
Continuation of :  
Application No. 10/646,787 :  
Filed August 25, 2003 :  
  
Filed: January 16, 2004 : Examiner: To be assigned  
  
For: **METHOD AND SOFTWARE FOR PARTITIONED FLOATING-POINT**  
**MULTIPLY-ADD OPERATION**

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop NEW APPLICATIONS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Continuation of  
Application No. 10/646,787  
Filed August 25, 2003  
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The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 10/646,787 , filed August 25, 2003 , which is relied upon for an earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR 1.98(d).

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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**Date: January 16, 2004**

WDC99 867108-1.043876.0162

INFORMATION DISCLOSURE CITATION IN AN APPLICATION			ATTY. DOCKET NO. <b>43876-162</b>	SERIAL NO. <b>Continuation of Serial No. 10/646,787</b>		
			APPLICANT <b>HANSEN, et al.</b>			
(PTO-1449)			FILING DATE <b>January 16, 2004</b>		GROUP <b>To be assigned</b>	
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	4,025,772	05/24/77	Constant			
	4,489,393	12/18/84	Kawahara, et al.			
	4,701,875	10/20/87	Konishi, et al.			
	4,727,505	02/23/88	Konishi, et al.			
	4,876,660	10/24/89	Owens, et al.			
	4,893,267	01/09/90	Alsup, et al.			
	4,956,801	09/11/90	Priem et al.			
	4,969,118	11/06/90	Montoye, et al.			
	4,975,868	12/04/90	Freerksen			
	5,032,865	07/16/91	Schlunt			
	5,157,388	10/20/92	Kohn			
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	5,268,855	12/07/93	Mason, et al.			
	5,268,995	12/07/93	Diefendorff, et al.			
<b>FOREIGN PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation
						<input type="checkbox"/> Yes <input type="checkbox"/> No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
	Parallel Computers for Graphics Applications, Adam Levinthal, Pat Hanrahan, Mike Paquette, Jim Lawson, Pixar San Rafael, California, 1987					
	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen, IEEE Micro, April 1992, 40-63					
	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.					
	IBM Creates PowerPC Processors for AS/400, Two New CPU's Implement 64-Bit Power PC with Extensions by Linley Gwennap, Microprocessor Report July 31, 1995, 15-16					
EXAMINER			DATE CONSIDERED			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.  
WDC99 867110-1.043876.0162

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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,408,581	04/18/95	Suzuki, et al.			
	5,423,051	06/06/95	Fuller, et al.			
	5,426,600	06/20/95	Nakagawa, et al.			
	5,500,811	03/19/96	Corry			
	5,557,724	09/17/96	Sampat, et al.			
	5,588,152	12/24/96	Dapp, et al.			
	5,592,405	01/07/97	Gove, et al.			
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	5,642,306	06/24/97	Mennemeier, et al.			
	5,666,298	09/09/97	Peleg, et al.			
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	5,673,407	09/30/97	Poland, et al.			
	5,675,526	10/07/97	Peleg, et al.			
	5,680,338	10/21/97	Agarwal, et al.			
<b>FOREIGN PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation
						Yes
	0 474 246 A2	06/09/91	EP			
	0 654 733 A1	05/07/94	EP			
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
	The Visual Instruction Set (VIS) in UltraSPARTM, L. Kohn, G. Maturana, M. Tremblay, A. Prabhu, G. Zyner, IEEE, May 3, 1995, 462-469					
	Osborne McGraw-Hill, i860TM Microprocessor Architecture, Neal Margulis, Foreword by Les Kohn, 1990, 8-10; 171-175; 182-183					
	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3) Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998, 5-26					
	Ruby B. Lee, "Accelerating Multimedia with Enhanced Microprocessors", IEEE Micro, April 1995, 22-32.					
EXAMINER	DATE CONSIDERED					

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U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
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	5,734,874	03/31/98	Van Hook, et al.			
	5,757,432	05/26/98	Dulong, et al.			
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	5,818,739	10/06/98	Peleg, et al.			
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	5,835,782	11/10/98	Chu Lin, et al.			
	5,886,732	03/23/99	Humbleman			
	5,922,066	07/13/99	Cho, et al.			
	5,983,257	11/09/99	Dulong, et al.			
	6,016,538	01/18/00	Guttag, et al.			
	6,092,094	07/18/00	Ireton			
	6,401,194 B1	06/04/02	Nguyen, et al.			
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